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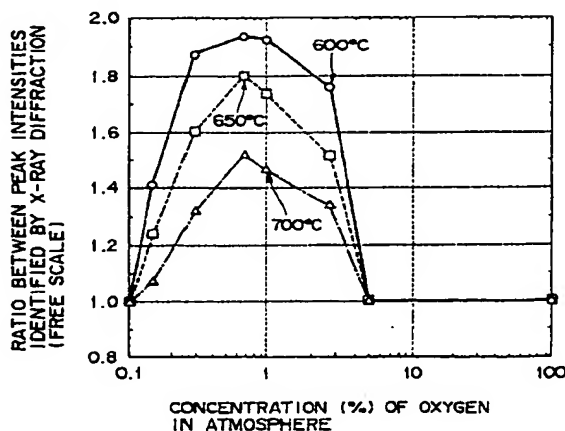
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80538 München (DE)**(54) METHOD FOR MANUFACTURING OXIDE DIELECTRIC DEVICE, AND MEMORY AND SEMICONDUCTOR DEVICE USING THE DEVICE**

(57) The temperature at which an oxide dielectric thin film is formed can be made bottom than conventional by reducing the concentration of oxygen in an atmosphere for forming the thin film. As a result, there can be formed an oxide dielectric thin film which has a crystal structure preferentially oriented at a crystal plane allowing a polarization axis to be directed in the vertical direction, eliminates any reaction with an electrode material, and controls the growth of crystal grains. The use of such an oxide dielectric thin film can provide an oxide dielectric element having a high spontaneous polarization and a small coercive field.

Consequently, it is possible to achieve a dielectric element having a high density of integration for detecting reading and writing operations, and a semiconductor device using the same.

FIG. 1



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Description

Technical Field

5 [0001] The present invention relates to a method of producing an oxide dielectric element, and a memory and a semiconductor device using the element.

Background Art

10 [0002] One type of recent semiconductor memory is a ROM (Read Only Memory) which makes use of a non-volatile characteristic allowing retention of data even in the OFF-state of a power supply. The ROM, however, has problems that the number of re-writing operations is largely limited, a re-writing speed is low, and the like. Another type of recent semiconductor memory is a RAM (Random Access Memory) having the advantage of enabling re-writing of data at a high speed. An oxide dielectric substance is used as a material of a storing capacitor of the RAM. The oxide dielectric substance is classified into a high dielectric substance having a high dielectric constant and a ferroelectric substance having a hysteresis of polarization. In particular, there are known a DRAM using a high dielectric substance and a non-volatile RAM using a ferroelectric substance. First, the non-volatile RAM using a ferroelectric substance will be described below. Such a non-volatile RAM is advantageous in that a non-volatile characteristic is obtained by making use of the hysteresis effect of the ferroelectric substance; the number of re-writing operations is as large as 10^{10} to 10^{12} ; and the re-writing speed is μs (1/1,000,000 sec) or bottom in comparison with other types, and therefore, the non-volatile RAM is expected as the future ideal memory. Development has been made to further enhance the capacity, non-volatile characteristic, and re-writing speed of the non-volatile RAM. The non-volatile RAM, however, has a large problem that there occurs a film fatigue with the increased number of re-writing operations, thereby deteriorating spontaneous polarization (Pr) of the ferroelectric substance. To enhance the capacity and durability of the non-volatile RAM, it is known to adopt (1) a ferroelectric material having a large spontaneous polarization (Pr) and (2) a ferroelectric material having a fatigue-free. As the ferroelectric material having a large spontaneous polarization (Pr) and a fatigue-free, an oxide having a perovskite structure is extensively available. In particular, there is known a Bi-layered ferroelectric substance, $\text{SrBi}_2\text{Ta}_2\text{O}_9$, having a crystal structure in which a plurality of simple lattices of the perovskite structure are layered. This material has a crystal anisotropy allowing the spontaneous polarization Pr to be directed only in the direction perpendicular to the c-axis, and is good in film fatigue characteristic although the Pr value is not necessarily large. Examples using such a material are disclosed in WO93/12542 (PCT/US92/10627) and Japanese Patent Laid-open No. Hei 5-24994.

20 [0003] Meanwhile, the DRAM using a high dielectric substance has ridden on the wave requiring a large capacity, for example, 16 Mbits, further 64 Mbits along with the progress of a high density and high integration technique. To meet such a requirement, there has been a strong demand for making finer the geometries of a circuit component of the DRAM, and particularly, the geometries of a capacitor for storing information. To achieve the finer-geometries of the capacitor, attempts have been made to make thin a film of a dielectric material, to adopt a material having a high dielectric constant, and to assemble the structure having top and bottom electrodes and a dielectric substance in not the two dimensions but the three dimensions. For example, a high dielectric substance, BST [$(\text{Ba}/\text{Sr})\text{TiO}_3$], having a crystal structure composed of a simple lattice of the perovskite structure is known as a material having a dielectric constant (ϵ) larger than that of a conventional high dielectric substance, $\text{SiO}_2/\text{Si}_3\text{N}_4$. An example using such a high dielectric substance has been reported in IEDM (International Electron Device Meeting) Tech. Dig.: 823, 1991.

Disclosure of Invention

45 [0004] The present invention relates to a method of producing an oxide dielectric element, and a memory and a semiconductor device using the same. In particular, the present invention is applicable to high dielectric elements such as a DRAM, which makes use of a high dielectric constant and a low leakage current density, and a non-volatile RAM, which makes use of a high spontaneous polarization and a low coercive field; and a memory and a semiconductor device using the high dielectric element or the ferroelectric element.

50 [0005] Ferroelectric thin films and high dielectric thin films have been required to be heated up to high temperatures, for example, about 650°C for a thin film of $\text{Pb}(\text{Zr}/\text{Ti})\text{O}_3$, about 600°C for a thin film of $(\text{Ba}/\text{Sr})\text{TiO}_3$, and about 800°C for a thin film of $\text{SrBi}_2\text{Ta}_2\text{O}_9$. That is to say, in formation of a thin film of the above material having a perovskite type crystal structure, the material must be heated up to a temperature of 600°C or higher for promoting crystallization. The heat treatment at such a high temperature, however, causes various problems. For example, in formation of a film by a vapor deposition process, a bottom electrode is exposed to an oxidizing atmosphere at a high temperature at the initial stage of the film formation, to be thus peeled off. Furthermore, when a film of $\text{SrBi}_2\text{Ta}_2\text{O}_9$ is formed at a high temperature of 800°C as conventional, Bi is evaporated to cause a deviation of the film composition, so that the starting content of Bi

is required to be made excessive. As a result, after the film formation at the high temperature, the excessive Bi exists as an irregular phase containing Bi in a large amount at grain boundaries of the ferroelectric layer, which causes degradation of withstand voltage characteristic, and further, a transient layer is formed by diffusion of elements at the inter-plane between the ferroelectric thin film and each of the top and bottom electrodes, which reduces the spontaneous polarization (Pr) to thereby degrade the original characteristics of the ferroelectric element, increases the coercive field (Ec), and causes the film fatigue. For this reason, the number of re-writing operations performed by reversing an electric field is largely limited. Further, the heat treatment at a high temperature arises problems that (a) the formation of the reaction layer reduces the dielectric constant and spontaneous polarization, and (b) the growth of crystal grain increases a leakage current density. This leads to the increased operational voltage, thus making it difficult to achieve the high integration of the element.

[0006] The present invention has been made on the basis of the above knowledge, and an object of the present invention is to provide a method of producing an oxide dielectric element having good characteristics, particularly, a ferroelectric element having a high spontaneous polarization and a low coercive field or a high dielectric element having a high dielectric constant and a good withstand voltage characteristic; and to provide a memory and a semiconductor device using the above oxide dielectric element.

[0007] According to one feature of the present invention, there is provided a method of producing an oxide dielectric element, particularly, a ferroelectric element having a high spontaneous polarization and a low coercive field or a high dielectric element having a high dielectric constant and a good withstand voltage characteristic, characterized in that a ferroelectric thin film as a main part of the ferroelectric element or a high dielectric thin film as a main part of the high dielectric element is formed in a low oxygen concentration atmosphere at a temperature of 650°C or less for the ferroelectric thin film or 600°C or less for the high dielectric thin film. In this case, to maximize the occupied ratio of a perovskite structure in the entire crystal phase in the thin film and hence to enhance electric characteristics of the element, the concentration of oxygen in the low oxygen concentration atmosphere may be preferably set in a range more than 0.1% and less than 5.0%.

[0008] According to another feature of the present invention, there is provided a method of producing an oxide dielectric element, characterized in that the low oxygen concentration atmosphere is variable by adjusting a mixing ratio of oxygen to inert gas and the heat treatment is carried out atmospheric pressure. With this configuration, the production method can be made very simple.

[0009] According to a further feature of the present invention, there is provided a method of producing an oxide dielectric element, characterized in that the ferroelectric thin film or high dielectric thin film formed in accordance with the above-described production method is re-heated in an activated oxygen atmosphere of O₃, N₂O, radical oxygen or the like. With this configuration, the quality of the ferroelectric or high dielectric thin film can be enhanced.

[0010] According to the present invention, the ferroelectric thin film is characterized in that it is expressed by a chemical structural formula of (AO)²⁺(BCO)²⁻ where A is one kind selected from a group consisting of Bi, Ti, Hg, Pb, Sb and As; B is at least one kind or more selected from a group consisting of Pb, Ca, Sr, Ba and rare earth elements; and C is at least one kind or more selected from a group consisting of Ti, Nb, Ta, W, Mo, Fe, Co and Cr; or expressed by a chemical structural formula of (Pb/A)(Zr/Ti)O₃ where A is one kind selected from a group consisting of La, Ba and Nb.

[0011] The high dielectric thin film is characterized in that it is expressed by a chemical structural formula of (Ba/Sr)TiO₃.

[0012] The high dielectric thin film obtained according to the present invention is characterized in that it has a dielectric constant larger than that of Ta₂O₅ conventionally used.

[0013] Each of top and bottom electrode materials used in the present invention is characterized in that it is at least one kind of metals selected from a group consisting of Pt, Au, Al, Ni, Cr, Ti, Mo and W; at least one kind of conductive oxides of a single element selected from a group consisting of Ti, V, Eu, Cr, Mo, W, Ph, Os, Ir, Pt, Re, Ru and Sn; or at least one kind of conductive oxides having a perovskite structure selected from a group consisting of ReO₃, SrReO₃, BaReO₃, LaTiO₃, SrVO₃, CaCrO₃, SrCrO₃, SrFeO₃, La_{1-x}Sr_xCoO₃ (0<x<0.5), LaNiO₃, CaRuO₃, SrRuO₃, SrTiO₃ and BaPbO₃. In the case of using the above conductive oxide of a single element or the above conductive oxide having a perovskite structure, the oxide is characterized in that it has a resistivity of 1m Ω · cm or less for ensuring the function of an electrode material.

[0014] According to the present invention, the method of producing the ferroelectric thin film or high dielectric thin film is characterized in that the thin film is formed by a sputtering process, a Pulsed Laser deposition process or a MOCVD (Metal Organic Chemical Vapor Deposition) process in an atmosphere of mixed gas of oxygen and inert gas. The thin film may be formed by a spin coating process or a dip coating process using metal alkoxide or organic acid salt as a starting material in an atmosphere of mixed gas of oxygen and inert gas under normal pressure.

[0015] In the method of producing the ferroelectric thin film or high dielectric thin film according to the present invention, the re-heating treatment is performed by a sputtering process, a Pulsed Laser deposition process or a MOCVD (Metal Organic Chemical Vapor Deposition) process in ECR oxygen plasma. The re-heating treatment may be performed by a spin coating process or a dip coating process using metal alkoxide or organic acid salt as a starting material.

by irradiation with light in a ultraviolet region.

Brief Description of Drawings

[0016]

Fig. 1 is a graph showing a change in crystal structure of a ferroelectric thin film depending on a concentration of oxygen in an atmosphere according to the present invention;

Fig. 2 is a sectional view showing a ferroelectric element of the present invention;

Fig. 3 is a sectional view showing a high dielectric element of the present invention;

Fig. 4 is a sectional view showing a ferroelectric element in which conductive oxide of the present invention is used for an electrode;

Fig. 5 is a schematic view showing a microstructure of the ferroelectric thin film of the present invention;

Fig. 6 is a sectional view showing a ferroelectric memory of the present invention;

Fig. 7 is a sectional view showing a high dielectric memory of the present invention;

Fig. 8 is a graph showing a degree of orientation of the (105) plane of the crystal structure depending on the concentration of oxygen in an atmosphere according to the present invention;

Fig. 9 is a graph showing a relationship between a voltage and a leakage current density according to the present invention;

Figs. 10(a) and 10(b) are views showing a non-contact type semiconductor device using the ferroelectric element of the present invention; and

Fig. 11 is a graph showing the result of measuring the number of cycles of the ferroelectric element of the present invention.

Best Modes for Carrying Out the Invention

[0017] Hereinafter, the present invention will be described, while not limited thereto, by way of embodiments with reference to the drawings.

[0018] Principal reference numerals used in the drawings designate as follows:

[0019] 21, 31, 61, 71 and 1008, an top electrode; 41, an top electrode (conductive oxide); 22, 42, 62 and 1007, a ferroelectric thin film; 32 and 72, a high dielectric thin film; 23, 33, 63, 73 and 1006, a bottom electrode; 43, a bottom electrode (conductive oxide); 24, 34 and 44, an underlying substrate; 25 and 45, a ferroelectric element; 35, a high dielectric element; 64, 74 and 1002, an Si substrate; 65 and 75, a source region; 66 and 76, a drain region; 67 and 79, polycrystal Si; 68, 77, and 78, SiO₂; 1001, a non-contact type semiconductor device; 1003, a diffusion layer; 1004, an SiO₂ gate layer; 1005, a gate electrode; 1009 and 1010, an SiO₂ insulating layer; and 1011, an aluminum interconnection.

(First Embodiment)

[0020] One embodiment of the present invention will be described.

[0021] The feature of the present invention will be described in detail below. The occupied ratio of a perovskite structure in the entire crystal phase of the thin film can be increased by controlling an atmosphere for forming a ferroelectric thin film or a high dielectric thin film as means of the present invention in such a manner as to reduce the concentration of oxygen contained in the atmosphere. For a ferroelectric substance SrBi₂Ta₂O₉, when the concentration of oxygen in the atmosphere is made bottom, formation of a liquid phase caused by decomposition of oxides is promoted even at a bottom temperature, to thereby achieve crystallization of the ferroelectric substance at a temperature bottom than conventional, that is, 800°C. The reduction in formation temperature is also effective to prevent reaction between the ferroelectric substance and each of top and bottom electrodes.

[0022] A ferroelectric thin film used in this embodiment is expressed by a chemical structural formula: (AO)²⁺(BCO)²⁻, where A is Bi, B is Sr, and C is Ta. Hereinafter, there will be described a method of producing such a ferroelectric thin film. Fig. 2 shows the structure of a ferroelectric element according to this embodiment, in which a bottom electrode is formed on an underlying substrate, the ferroelectric thin film is formed on the bottom electrode, and an top electrode is formed on the ferroelectric thin film. In addition, reference numeral 24 designates the underlying substrate. An Si base, on which an SiO₂ film was formed by thermal oxidation, was used as the underlying substrate 24. The bottom electrode 23 (Pt) having a thickness of 2,000 Å was formed on the underlying substrate 24 at room temperature by sputtering. To form the ferroelectric dielectric thin film 22 on the bottom electrode 23, the surplane of the bottom electrode 23 was spin-coated with a metal alkoxide solution having a composition of Bi: Sr: Ta = 2: 1: 2 at 3,000 rpm (the number of rotation per minute) for 30 sec. The resultant substrate was dried at 150°C for 10 min, and then, preheated in air or oxygen

at a temperature bottom than the crystallization temperature of the ferroelectric thin film, concretely, at 500°C for 15 min. The above procedure was repeated three times to form a precursor thin film having a thickness of 2,400 Å. Finally, the precursor thin film was heat-treated in an atmosphere containing oxygen at a varying concentration at 650°C for 1 hr to produce a ferroelectric thin film. The crystal structure of the ferroelectric thin film was identified by X-ray diffraction.

[0023] Fig. 1 shows a change in occupied ratio of a perovskite structure in the entire crystal phase of a thin film depending on the concentration of oxygen in atmospheric gas. The reduced concentration of oxygen functions to increase the occupied ratio of the perovskite structure. More specifically, the increase in occupied ratio of the perovskite structure is maximized at the concentration of oxygen in a range of 0.2 to 3.0%. As a result, the concentration of oxygen in the atmospheric gas for forming the ferroelectric thin film may be preferably in a range of more than 0.1% and less than 5.0%. When the concentration of oxygen is equal to or less than 0.1%, the amount of oxygen required for forming the perovskite structure is insufficient, so that it is difficult to form the perovskite structure. When it is equal to or more than 5.0%, the effect of forming the perovskite structure is no longer obtained. Fig. 1 also shows a change in occupied ratio of the perovskite structure in the entire crystal phase of the thin film depending on the concentration of oxygen in the atmosphere when the formation temperature is changed in a range of 600 to 700°C. The effect of the low oxygen concentration exerted on the occupied ratio of the perovskite structure becomes more effective as the formation temperature becomes bottom. According to this embodiment, it may be desirable to form a ferroelectric thin film at a temperature of 650°C or less and to form a high dielectric thin film at a temperature of 600°C or less. In this case, the bottom limit of the formation temperature may be desirable to be set at 400°C, because it is difficult to form the perovskite structure by heat treatment at a temperature less than 400°C.

[0024] Fig. 8 shows a relationship between a degree of orientation of the (105) plane of the perovskite structure and a concentration of oxygen. The degree of orientation is expressed by a ratio of a peak intensity $I(105)$ of the (105) plane to a peak intensity $I(\text{total})$ of the total crystal planes identified by X-ray diffraction. When the concentration of oxygen is less than 5%, the degree of orientation of the (105) plane becomes large. This means that the crystal structure of the $\text{SrBi}_2\text{Ta}_2\text{O}_9$ ferroelectric thin film formed at a low oxygen concentration is strongly oriented at the (105) plane. The reason for this is that a liquid phase is partially produced by decomposition of oxides of the components at a low oxygen concentration and crystal growth is originated from the liquid phase, to thereby make easy preferential growth of the (105) plane. That is to say, the low oxygen concentration is effective to make easy the orientation of the crystal structure at the (105) plane. The $\text{SrBi}_2\text{Ta}_2\text{O}_9$ ferroelectric thin film having a layered perovskite structure exhibits a crystal anisotropy allowing the polarization axis to be directed only in parallel to the Bi-O layer (perpendicular to the c-axis) because of the symmetry of crystals. However, since the $\text{SrBi}_2\text{Ta}_2\text{O}_9$ ferroelectric thin film in this embodiment is preferentially oriented at the (105) plane, such a thin film can exhibit good characteristics. In addition, even a different ferroelectric material can be preferentially oriented at a crystal plane allowing the polarization axis to be directed in the vertical direction.

[0025] Next, a Pt film having a thickness of 2,000 Å was formed on the ferroelectric thin film 22 expressed by the chemical structural formula, $(\text{BiO})^{2+}(\text{SrTaO})^{2-}$, at room temperature by sputtering, to form the top electrode 21 on the ferroelectric thin film 22. In this way, a ferroelectric element 25 was obtained. The spontaneous polarization (Pr) and the coercive field (Ec) of the ferroelectric element thus obtained were measured at room temperature. The results are shown in Table 1.

Table 1

Conc. of O_2 (%)	0.15	0.2	0.7	1.0	3.0	5.0
Pr ($\mu\text{C}/\text{cm}^2$)	7	17	20	18	17	6
Ec (kV/cm)	70	52	45	50	53	74
Number of re-writing operations	$1\text{E}+14$	$1\text{E}+14$	$1\text{E}+14$	$1\text{E}+14$	$1\text{E}+14$	$1\text{E}+13$

[0026] The value Pr is a polarization quantity obtained at a positive or negative maximum applied voltage in the hysteresis of the Pr - V curve. The value Pr is high and the value Ec is low at a concentration of oxygen in a range of 0.2 to 3.0%. This corresponds to the results of X-ray diffraction. In particular, the ferroelectric thin film formed at an oxygen concentration of 0.7% exhibits a value Pr of 20 $\mu\text{C}/\text{cm}^2$ and a value Ec of 45 kV/cm. Each ferroelectric thin film formed in this embodiment was measured in terms of the number of cycles in which a voltage of 136 kV/cm was repeatedly applied to the thin film with the polarity of the voltage reversed. Fig. 11 typically shows the measurement result of the ferroelectric thin film formed at the oxygen concentration of 0.7%. For each ferroelectric thin film formed at an oxygen concentration in a range of 0.15 to 3.0%, degradation of the Pr characteristic is not observed until the number of cycles reaches 10^{14} times.

[0027] A ferroelectric element including a ferroelectric thin film having a chemical structural formula of $(\text{AO})^{2+}(\text{SrTaO})^{2-}$ where the element of the A site is selected from a group consisting of Tl, Hg, Pb, Sb and As was produced in the

same procedure as that described above. The values P_r and E_c of each ferroelectric element thus obtained were measured. As a result, the value P_r was in a range of 19 to 21 $\mu\text{C}/\text{cm}^2$ and the value E_c was in a range of 44 to 48 kV/cm.

[0028] A ferroelectric element including a ferroelectric thin film expressed by a chemical structural formula: $(\text{BiO})^{2+}(\text{BTaO})^{2-}$, where the element of the B site is selected from a group consisting of Pb, Ca and Ba was produced in the same procedure as that described above. The values P_r and E_c of the ferroelectric element thus obtained were measured. As a result, the value P_r was in a range of 18 to 22 $\mu\text{C}/\text{cm}^2$ and the value E_c was in a range of 43 to 47 kV/cm.

[0029] A ferroelectric element including a ferroelectric thin film expressed by a chemical structural formula: $(\text{BiO})^{2+}(\text{SrCO})^{2-}$, where the element of the C site is selected from a group consisting of Ti, Nb, W, Mo, Fe, Co and Cr was produced in the same procedure as that described above. The values P_r and E_c of the ferroelectric element thus obtained were measured. As a result, the value P_r was in a range of 17 to 22 $\mu\text{C}/\text{cm}^2$ and the value E_c was in a range of 42 to 49 kV/cm.

[0030] Moreover, in the first embodiment, since the ferroelectric thin film can be formed at a low temperature by reducing the concentration of oxygen, there is no problem in formation of a transient layer or diffusion of the elements, so that there may be adopted a structure in which no diffusion preventive layer is provided between the thin film and the underlying substrate.

(Second Embodiment)

[0031] A ferroelectric thin film used in this embodiment is expressed by a chemical structural formula: $(\text{Pb}/\text{A})(\text{Zr}/\text{Ti})\text{O}_3$, where A is La. Hereinafter, there will be described a method of producing such a ferroelectric thin film. In Fig. 2 being a sectional view of the ferroelectric element, reference numeral 24 designates an underlying substrate. An Si base, on which an SiO_2 film was formed by thermal oxidation, was used as the underlying substrate 24. A Pt film having a thickness of 2,500 Å, as a bottom electrode 23, was formed on the underlying substrate 24 in vacuum at room temperature by sputtering. To form a ferroelectric dielectric thin film 22 on the bottom electrode 23, the surface of the bottom electrode 23 was spin-coated with a metal alkoxide solution having a composition of Pb: La: Zr: Ti = 0.95: 0.05: 0.52: 0.48 at 2,500 rpm for 30 sec. The resultant substrate was dried at 140°C for 13 min, and then, pre-heated in air or oxygen at a temperature bottom than the crystallization temperature of the ferroelectric thin film 22, concretely, at 450°C for 20 min. The above procedure was repeated three times to form a precursor thin film having a thickness of 1,700 Å. Thereafter, the precursor thin film was heat-treated in a low oxygen atmosphere at 550°C to produce the ferroelectric thin film 22 expressed by the chemical structural formula:

$(\text{Pb}/\text{A})(\text{Zr}/\text{Ti})\text{O}_3$. The crystal structure of the ferroelectric thin film was identified by X-ray diffraction. The results showed that, like the first embodiment, the occupied ratio of the perovskite structure in the entire crystal phase of the thin film was rapidly increased at the concentration of oxygen in a range of 0.2 to 3.0%. Next, a Pt film having a thickness of 2,000 Å, as an top electrode 21, was formed on the ferroelectric thin film 22 expressed by the chemical structural formula, $(\text{Pb}/\text{A})(\text{Zr}/\text{Ti})\text{O}_3$, in vacuum at room temperature by sputtering. In this way, a ferroelectric element 25 was obtained. The spontaneous polarization (P_r) and the coercive field (E_c) of the ferroelectric element thus obtained were measured. As a result, the value P_r was 20 $\mu\text{C}/\text{cm}^2$ and the value E_c was 50 kV/cm at the oxygen concentration of 0.7%. Further, the dielectric constant (ϵ) of the ferroelectric element was measured at room temperature. The results are shown in Table 2.

Table 2

Conc. of O_2 (%)	0.15	0.2	0.7	1.0	3.0	5.0
ϵ	1320	1560	1590	1570	1564	1290
$J(\text{A}/\text{cm}^2)$ at 3V	7E-7	2E-7	1E-7	3E-7	4E-7	3E-6

[0032] The value ϵ was 1590 at the oxygen concentration of 0.7%. Further, the ferroelectric element was measured in terms of a relationship between an applied voltage and a leakage current density. As a result, the leakage current density was 1×10^{-7} A/cm² or less at an applied voltage of 3 V. Accordingly, it was confirmed that the ferroelectric element in this embodiment was very good in withstand voltage characteristic.

[0033] A ferroelectric element including a ferroelectric thin film expressed by a chemical structural formula: $(\text{Pb}/\text{A})(\text{Zr}/\text{Ti})\text{O}_3$, where A is Ba or Nb was produced in the same procedure as that described above. The values P_r and E_c of each ferroelectric element were measured. As a result, the value P_r was 20 $\mu\text{C}/\text{cm}^2$ and the value E_c was 51 kV/cm. The dielectric constant of the ferroelectric element was also estimated at room temperature. As a result, the ferroelectric element exhibited a dielectric constant which was as high as 1590 to 1610 at an oxygen concentration of 0.2

to 3.0%. In particular, even the ferroelectric thin film expressed by the chemical structural formula: $(\text{Pb}/\text{A})(\text{Zr}/\text{Ti})\text{O}_3$, where A is Ba, Nb or Ti, which was orientated at the (111) plane, exhibited a high polarization characteristic.

(Third Embodiment)

[0034] A method of producing a high dielectric thin film having a composition of $(\text{Ba}_{0.5}\text{Sr}_{0.5})\text{TiO}_3$ according to this embodiment will be described below. In Fig. 3 being a sectional view of a high dielectric element, reference numeral 34 designates an underlying substrate. The same Si base as that in the second embodiment was used as the underlying substrate 34. A Pt film having a thickness of 2,000 Å, as a bottom electrode 33, was formed on the underlying substrate 34 in vacuum at room temperature by sputtering. To form a ferroelectric dielectric thin film 32 on the bottom electrode 33, a precursor thin film having a thickness of 100 nm was formed on the bottom electrode 33 in mixed gas of oxygen and argon at a temperature of 300°C and at a pressure of 0.55 Pa. To form a perovskite structure, the precursor thin film was heat-treated in the atmosphere containing oxygen at a low concentration at 500 °C, to form the high dielectric thin film having the composition of $(\text{Ba}_{0.5}\text{Sr}_{0.5})\text{TiO}_3$. The crystal structure of the high dielectric thin film was examined by X-ray diffraction. The results showed that, like the first embodiment, the occupied ratio of the perovskite structure in the entire crystal phase of the thin film began to be increased as the oxygen concentration was less than 5%, and it was maximized at an oxygen concentration of 0.2 to 3.0%. Then, a Pt film having a thickness of 2,000 Å, as an top electrode 31, was formed on the high dielectric thin film 32 having the composition of $(\text{Ba}_{0.5}\text{Sr}_{0.5})\text{TiO}_3$ in vacuum at room temperature by sputtering. In this way, a high dielectric element 35 was obtained. The dielectric constant (ϵ) of the high dielectric element 35 was measured at room temperature. The results are shown in Table 3.

Table 3

Conc. of O_2 (%)	0.15	0.2	0.7	1.0	3.0	5.0
ϵ	310	493	520	503	480	253

[0035] The high dielectric element exhibited a dielectric constant which was as high as 480 to 520 at an oxygen concentration of 0.2 to 3.0%.

(Fourth Embodiment)

[0036] A ferroelectric thin film used in this embodiment is expressed by a chemical structural formula: $(\text{AO})^{2+}(\text{BCO})^{2-}$, where A is Bi, B is Sr, and C is Nb. Hereinafter, there will be described a method of producing such a ferroelectric thin film. In Fig. 4 being a sectional view of a ferroelectric element, reference numeral 44 designates an underlying substrate. An Si base, on which an SiO_2 film was formed by thermal oxidation, was used as the underlying substrate 44. A film (thickness: 1,700 Å) made from conductive oxide of a single element, RuO, was formed on the underlying substrate 44 in an oxygen gas atmosphere at 450°C by sputtering. To form a ferroelectric thin film on the bottom electrode 43, the surplene of the bottom electrode 43 was spin-coated with a metal alkoxide solution containing Bi, Sr and Nb at 3,000 rpm for 25 sec. The resultant substrate was dried at 150°C for 10 min, and then, pre-heated in air or oxygen at 450°C for 10 min. The above procedure was repeated three times to form a precursor thin film having a thickness of 2,300 Å. The precursor thin film was heated in a low oxygen concentration atmosphere containing argon gas and oxygen at a concentration of 0.7% at 600°C to produce a ferroelectric thin film 42, $(\text{BiO})^{2+}(\text{SrNbO})^{2-}$, having the perovskite structure. Next, a film (thickness: 1,700 Å) made from the conductive oxide of the single element RuO, as an top electrode 41, was formed on the ferroelectric thin film 42 in an oxygen gas atmosphere at 450°C by sputtering. In this way, a ferroelectric element 45 was produced. The spontaneous polarization (P_r) and the coercive field (E_c) of the ferroelectric element 45 thus obtained were measured at room temperature. As a result, the value P_r was 19 $\mu\text{C}/\text{cm}^2$ and the value E_c was 46 kV/cm.

[0037] Each ferroelectric element having the same structure as that described above except that the electrode material was changed from RuO into any one of TiO_x , VO_x , EuO , CrO_2 , MoO_2 , WO_2 , PhO , OsO , IrO , PtO , ReO_2 , RuO_2 and SnO_2 was produced in the same procedure as that described above. The values P_r and E_c of each ferroelectric element were measured. As a result, the value P_r was in a range of 18 to 22 $\mu\text{C}/\text{cm}^2$ and the value E_c was in a range of 44 to 48 kV/cm. As described above, by use of a conductive oxide of a single element, which has a resistivity of $1\text{m}\Omega \cdot \text{cm}$ or less for ensuring a function as a metal or an electrode material, or one kind of conductive oxide having the perovskite structure, as the material of each of the top and bottom electrodes used in this embodiment, there can be produced an oxide dielectric element having good electric characteristics.

(Fifth Embodiment)

[0038] A bottom electrode (Pt) was formed on an underlying substrate in accordance with the same production method as that in the first embodiment. Next, to form a ferroelectric thin film expressed by a chemical structural formula: $(AO)^{2+}(BCO)^{2-}$, where A is Bi, B is Sr, and C is Ta, the bottom electrode was spin-coated with a metal alkoxide solution having the same composition as that in the first embodiment at 3,000 rpm for 35 sec. The resultant substrate was dried at 150°C for 10 min, and then, pre-heated in air or oxygen at 400°C for 10 min. The above procedure was repeated twice to form a precursor thin film having a thickness of 1,100 Å. The precursor thin film was heated in an atmosphere containing oxygen at a low concentration at 630°C to produce a ferroelectric thin film. For comparison, a ferroelectric thin film, which was prepared by forming a bottom electrode on an underlying substrate and forming a ferroelectric thin film having the same composition as that described above at a low oxygen concentration in accordance with the same procedure as that described above, was further heated in ECR oxygen plasma at 400°C. An top electrode (Pt) was formed on each ferroelectric thin film thus obtained, in the same procedure as that in the first embodiment, to thus produce a ferroelectric element having the cross-sectional structure shown in Fig. 2. The spontaneous polarization (Pr) and the coercive field (Ec) of each ferroelectric element were measured at room temperature. The results are shown in Table 4.

Table 4

Atmosphere	No heating	Radical oxygen	N ₂ O	O ₃
Pr($\mu\text{C}/\text{cm}^2$)	19	30	27	28
Ec(kV/cm)	43	35	34	31

[0039] The ferroelectric element re-heated in the ECR oxygen plasma is higher in spontaneous polarization and bottom in coercive field than the ferroelectric element without re-heating treatment. Similarly, the ferroelectric element was re-heated using each of O₃, radical oxygen, and N₂O (nitrous oxide). Each of the ferroelectric element thus re-heated exhibits a spontaneous polarization and a coercive field comparative to those of the ferroelectric element re-heated in the ECR oxygen plasma. As described above, the re-heating treatment of a ferroelectric thin film in an activated oxygen atmosphere having a strong oxidizing function forms the perovskite structure without any loss in oxygen, to thereby remarkably increase electric characteristics of the ferroelectric element including the ferroelectric thin film. The re-heating treatment may be preferably performed at a temperature equal to or less than the crystallization temperature of a ferroelectric thin film at a low oxygen concentration.

(Sixth Embodiment)

[0040] A bottom electrode (Pt) was formed on an underlying substrate in accordance with the same production method as that in the first embodiment. Next, to form a ferroelectric thin film expressed by a chemical structural formula: $(AO)^{2+}(BCO)^{2-}$, where A is Bi, B is Sr, and C is Ta, the bottom electrode was spin-coated with a metal alkoxide solution having a composition of Bi: Sr: Ta = 2.2: 1: 2 at 3,500 rpm for 25 sec. The resultant substrate was dried at 170°C for 10 min, and then, pre-heated at 450°C for 10 min. The above procedure was repeated three times to form a precursor thin film having a thickness of 2,200 Å. The precursor thin film was heat-treated in an atmosphere containing oxygen at a concentration of 0.7% at 650°C for 1 hr to form a ferroelectric thin film. For comparison, a precursor thin film prepared in accordance with the same procedure as that described above was heat-treated in an atmosphere of 100% oxygen at 650°C for each treatment time of 1 hr and 5 hr, to form a ferroelectric thin film. An top electrode (Pt) was formed on each of the ferroelectric elements thus obtained in the same procedure as that in the first embodiment, to thus produce a ferroelectric element having the cross-sectional structure shown in Fig. 2. The Pr of each ferroelectric element was measured at room temperature. As a result, for the ferroelectric element formed at the oxygen concentration of 0.7%, the value Pr was 22 $\mu\text{C}/\text{cm}^2$; while for the ferroelectric element (treatment time: 1 hr) formed at the oxygen concentration of 100%, the hysteresis curve of polarization was not observed and for the ferroelectric element (treatment time: 5 hr) formed at the oxygen concentration of 100%, the value Pr was as low as 10 $\mu\text{C}/\text{cm}^2$. In this way, the decrease in oxygen concentration has an effect of shortening the heat treatment time. The reason for this is that, as described above, the rate of crystal growth originated from a liquid phase caused by decomposition of oxides in the components is increased by the decrease in oxygen concentration, and accordingly, the thin film formed at a low oxygen concentration forms the perovskite structure at a short treatment time which is about one-fifth that of the thin film formed at the conventional oxygen concentration (100%), to thereby obtain high electric characteristics. The results of the component analysis for each ferroelectric thin film show that the ferroelectric thin film formed in the atmosphere of a low oxygen

concentration has a stoichiometric composition of Sr: Bi: Ta = 1: 2: 2; while the ferroelectric thin film formed in the atmosphere of 100% oxygen has a Bi-rich composition of Sr: Bi: Ta = 1: 2.2: 2. In this embodiment, a ferroelectric thin film is formed in an atmosphere of a low oxygen concentration at a low temperature, and accordingly, for example, an $\text{SrBi}_2\text{Ta}_2\text{O}_9$ ferroelectric thin film having a stoichiometric composition can be formed irrespective of the starting content of Bi. This eliminates the necessity of making excessive the starting content of Bi. Even if the starting content of Bi is excessive, it is possible to suppress formation of an irregular phase containing Bi in a large amount at grain boundaries of a ferroelectric layer after formation of a ferroelectric thin film, and hence to enhance the withstand voltage characteristic of the thin film. Further, since there is no reaction between the ferroelectric thin film and each of the top and bottom electrodes, it is possible to enhance the dielectric constant of the ferroelectric thin film.

[0041] In this embodiment, description is made by example of the $\text{SrBi}_2\text{Ta}_2\text{O}_9$ ferroelectric thin film; however, even for an oxide dielectric thin film expressed by a chemical structural formula: $\text{Pb}(\text{Zr}/\text{Ti})\text{O}_3$, $(\text{Ba}/\text{Sr})\text{TiO}_3$ or the like, the heat treatment time required for forming the thin film can be shortened.

(Seventh Embodiment)

[0042] A Pt bottom electrode having a thickness of 2,000 Å was formed on an underlying substrate composed of an Si base on which SiO_2 was formed, in accordance with the same production method as that in the first embodiment. To form a ferroelectric thin film on the Pt bottom electrode, the bottom electrode was spin-coated with a metal alkoxide solution having a composition of Bi: Sr: Ta = 2: 1: 2 at 2,000 rpm for 30 sec. The resultant substrate was dried at 150°C for 15 min, and then, pre-heated at 450°C for 20 min. The above procedure was repeated five times to form a precursor thin film having a thickness of 2,000 Å. The precursor thin film was heat-treated in an atmosphere containing oxygen at a concentration of 0.7% at 650°C for 1 hr to produce a ferroelectric thin film. For comparison, the same precursor thin film was heat-treated at each of 800°C and 720°C for 1 hr in an atmosphere of 100% oxygen, to form a ferroelectric thin film. A Pt top electrode having a thickness of 2,000 Å was formed on the surplane of each ferroelectric thin film by sputtering, to thus produce a ferroelectric element. The withstand voltage characteristic of each ferroelectric element thus obtained was measured. The results are shown in Fig. 9. The ferroelectric element formed at 650°C in the atmosphere of the oxygen concentration of 0.7% exhibits a leakage current density of 3.0×10^{-9} A/cm² even at a voltage of 5 V, and therefore, it is superior in withstand voltage characteristic to the ferroelectric element formed at the conventional heat treatment temperature of 800°C or 720°C.

[0043] Fig. 5 is a schematic view of a micro-structure of the $\text{SrBi}_2\text{Ta}_2\text{O}_9$ ferroelectric substance obtained in this embodiment. From the micro-structure shown in Fig. 5, it becomes apparent that crystal grains of the ferroelectric thin film obtained in the atmosphere of a low oxygen concentration at a low temperature have an average grain size of about 70 nm or less, that is, the crystal grains are smaller and denser than those of the ferroelectric thin film obtained at a high temperature. Accordingly, the ferroelectric thin film obtained in this embodiment is small in leakage current density and good in withstand voltage characteristic.

[0044] Further, as a result of measuring the withstand voltage of the $(\text{Ba}_{0.5}\text{Sr}_{0.5})\text{TiO}_3$ high dielectric thin film formed in the third embodiment, the leakage current density was 5.0×10^{-7} A/cm². Accordingly, it was confirmed that the above ferroelectric thin film was good in withstand voltage characteristic.

(Eighth Embodiment)

[0045] Fig. 6 is a sectional view of a ferroelectric memory using a ferroelectric element according to this embodiment. The ferroelectric memory has a structure including a MOS-transistor in which an oxide layer, a metal layer and an insulating layer are formed on a semiconductor field effect transistor structure, and a capacitor composed of the above ferroelectric element shown in Fig. 2. The production method will be described below. First, the surplane of an Si substrate 64 having a source region 65 and a drain region 66 was oxidized to form an SiO_2 film having a thickness of 260 Å. The SiO_2 film was mask-patterned to form a projecting SiO_2 film 68 at a central portion of the substrate. A polycrystal Si film 67 having a thickness of 4,500 Å was formed on the projecting Si film 68 by CVD. The ferroelectric element having a structure including the top electrode 61, ferroelectric thin film, and low electrode 63 produced in the first embodiment was formed on the polycrystal Si film 67. In this way, the ferroelectric memory using the ferroelectric element was obtained. This ferroelectric memory is advantageous in that a difference in capacitance caused by reversal of an electric field can be detected at double magnitude.

(Ninth Embodiment)

[0046] Fig. 7 is a sectional view showing a high dielectric memory using a high dielectric element according to this embodiment. The production method will be described below. First, the surplane of an Si substrate 74 having a source region 75 and a drain region 76 was oxidized to form an SiO_2 film having a thickness of 270 Å. The SiO_2 film was mask-

patterned to form a projecting SiO_2 film 78 at a central portion of the substrate. A polycrystal Si film 79 having a thickness of 4,600 Å was formed on the projecting Si film 78 by CVD, and then, the surplane of the Si substrate 74 was oxidized to form an SiO_2 film 77 having a thickness of 250 Å, thus producing a MOS transistor portion. The high dielectric element having the structure including the top electrode 71, high dielectric thin film 72 and bottom electrode 73 produced in the third embodiment was formed on a capacitor portion opposed to the semiconductor MOS portion thus obtained. In this way, a high dielectric memory using the high dielectric element was obtained. The high dielectric memory allows detection by a change in stored charge capacity obtained by a voltage of 3 V.

(Tenth Embodiment)

[0047] Fig. 10(a) shows a non-contact type semiconductor device 1001, and Fig. 10(b) shows the structure of a ferroelectric element contained in the non-contact type semiconductor device. The ferroelectric element was formed as follows: namely, an SiO_2 gate film 1004 was formed on an Si substrate 1002 having a diffusion layer 1003, and was mask-patterned to form a gate electrode 1005. A ferroelectric capacitor includes a Pt bottom electrode 1006, an ($\text{SrBi}_2\text{Ta}_2\text{O}_9$) ferroelectric thin film 1007 formed at a low oxygen concentration, and a Pt top electrode 1008. To separate the transistor from the capacitor, SiO_2 insulating layers 1009 and 1010 were formed, and the top electrode 1008 was connected to the diffusion layer 1003 via an aluminum interconnection 1011. A system using a non-contact semiconductor device includes a controller, a respositor containing a memory and a communication device, and an IC card containing the non-contact type semiconductor device, wherein a signal from the controller is transmitted to the IC card, and information necessary for the IC card is fed back to the controller on the basis of a command. The use of a non-volatile RAM for a memory element allows the reversal time of the ferroelectric substance to be one nanosecond or shorter. As a result, the system has various good performances that information reading operation is performed at the equidistance with information writing operation, the transmission speed of data is high, and occurrence of an error upon writing is extremely reduced.

[0048] In the above embodiment, description is made by way of the ferroelectric element having the Pt top electrode 1008, $\text{SrBi}_2\text{Ta}_2\text{O}_9$ ferroelectric thin film 1007, and bottom electrode 1006; however, there may be formed a high dielectric element having an top electrode, a high dielectric thin film, and a bottom electrode. The semiconductor device using the high dielectric element has a 30 fF stored charge capacity at a voltage of 3 V.

[0049] As described above, the use of the ferroelectric element in this embodiment can provide the non-contact type semiconductor having the good electric characteristics.

Industrial Applicability

[0050] As described above, since the liquid phase by decomposition of oxides in the components is produced and the rate of crystal growth originated from the liquid phase is increased when the oxide ferroelectric thin film and the oxide high dielectric thin film are formed in the atmosphere of the reduced concentration of oxygen, it is possible to form the oxide ferroelectric thin film and the oxide high dielectric thin film at temperatures bottom than conventional, particularly, 650°C or less and 600°C or less, respectively, and further, to shorten the heat treatment time required for forming the thin film. As a result, in the thin film formed according to the present invention, the crystal structure is preferentially oriented at the crystal plane allowing the polarization axis to be directed in the vertical direction; the average crystal grain size is controlled at an optimum value; and reaction with the electrode is prevented. This makes it possible to form the oxide dielectric element having the high dielectric constant, the high spontaneous polarization, and the small coercive field. The ferroelectric memory for detecting reading and writing operations can be produced by incorporating the above ferroelectric element in the semiconductor field effect transistor, and the high dielectric memory for detecting the reading and writing operations can be also produced by incorporating the above high dielectric element in the semiconductor MOS structure. Further, the semiconductor device using the ferroelectric memory or high dielectric memory as the non-contact type reading or writing memory can be produced.

[0051] In this way, the present invention is effective in application to the highly integrated ferroelectric element and high dielectric element, and the semiconductor device.

Claims

1. A method of producing an oxide dielectric element including an top electrode, an oxide dielectric thin film, and a bottom electrode, characterized in that

said oxide dielectric thin film is formed in an atmosphere containing oxygen at a concentration of more than 0.1% and less than 5% and at a temperature of 650°C or bottom.

2. A method of producing an oxide dielectric element including an top electrode, an oxide dielectric thin film, and a bottom electrode, characterized in that

said oxide dielectric thin film is formed in an atmosphere containing oxygen at a concentration of more than 0.1% and less than 5% and at a temperature of 600°C or bottom.

3. A method of producing an oxide dielectric element according to claim 1 or claim 2, wherein said oxide dielectric thin film is re-heated in an activated oxygen atmosphere.

4. A method of producing an oxide dielectric element according to claim 2 or claim 3, wherein said oxide dielectric thin film is expressed by a composition of (Ba/Sr)TiO₃.

5. An oxide dielectric element including an top electrode, an oxide dielectric thin film, and a bottom electrode, characterized in that

said oxide dielectric thin film has a composition of (Ba/Sr)TiO₃; and
the leakage current density of said oxide dielectric thin film is 10⁻⁶ A/cm² or less at a voltage of 5 V or bottom.

6. A method of producing an oxide dielectric element according to claim 1, wherein said oxide dielectric thin film is expressed by a chemical structural formula: (Pb/A)(Zr/Ti)O₃, where A is one kind selected from a group consisting of La, Ba and Nb.

7. An oxide dielectric element including an top electrode, an oxide dielectric thin film, and a bottom electrode, characterized in that

said oxide dielectric thin film is expressed by a chemical structural formula: (Pb/A)(Zr/Ti)O₃, where A is one kind selected from a group consisting of La, Ba and Nb; and
the leakage current density of said oxide ferroelectric thin film is 10⁻⁶ A/cm² or less at a voltage of 5 V or bottom.

8. An oxide dielectric element according to claim 5 or claim 7, wherein said oxide dielectric thin film has a crystal structure in which the a-axis is oriented at an angle of 0°, 45° or 90° with respect to said bottom electrode.

9. An oxide dielectric element including an top electrode, an oxide dielectric thin film, and a bottom electrode, characterized in that

said oxide dielectric thin film is expressed by a chemical structural formula: (AO)²⁺(BCO)²⁻, where A is one kind selected from a group consisting of Bi, Ti, Hg, Pb, Sb and As; B is at least one kind or more selected from a group consisting of Pb, Ca, Sr, Ba and rare earth elements; and C is at least one kind or more selected from a group consisting of Ti, Nb, Ta, W, Mo, Fe, Co and Cr.

10. An oxide dielectric element according to claim 9, wherein said oxide dielectric thin film has a perovskite structure composed of two or more of unit cells; and

the degree of orientation of the (105) plane is more than 45%.

11. An oxide dielectric element according to claim 9 or claim 10, wherein the crystal grain size of said oxide dielectric thin film is approximately 70 nm or smaller.

12. An oxide dielectric element according to any one of claims 9 to 11, wherein the leakage current density of said oxide dielectric thin film is 10⁻⁷ A/cm² or less at a voltage of 5 V or bottom.

13. A method of producing an oxide dielectric element including an top electrode, an oxide dielectric thin film, and a bottom electrode, characterized in that

said oxide dielectric thin film has a composition of (Ba/Sr)TiO₃; and
said oxide dielectric thin film is formed in an atmosphere containing oxygen at a concentration of more than 0.1% and less than 5% and at a temperature of 500°C or bottom.

14. A method of producing an oxide ferroelectric element according to claim 6, wherein the temperature at which said oxide dielectric thin film is formed is 550°C or bottom.

15. A method of producing an oxide dielectric thin film including an top electrode, an oxide dielectric thin film, and a bottom electrode, characterized in that

said oxide dielectric thin film is expressed by a chemical structural formula: $(AO)^{2+}(BCO)^{2-}$, where A is one kind selected from a group consisting of Bi, Ti, Hg, Pb, Sb and As; B is at least one kind or more selected from a group consisting of Pb, Ca, Sr, Ba and rare earth elements; and C is at least one kind or more selected from a group consisting of Ti, Nb, Ta, W, Mo, Fe, Co and Cr; and said oxide dielectric thin film is formed in an atmosphere containing oxygen at a concentration of more than 0.1% and less than 5% and at a temperature of 650°C or bottom.

16. A method of producing an oxide dielectric element according to claim 1 or claim 2, wherein each of said top electrode and said bottom electrode is made from at least one kind or more of metals selected from a group consisting of Pt, Au, Al, Ni, Cr, Ti, Mo and W.

17. A method of producing an oxide dielectric element according to claim 1 or claim 2, wherein each of said top electrode and said bottom electrode is made from an oxide or oxides of at least one kind or more of metals selected from a group consisting of Ti, V, Eu, Cr, Mo, W, Ph, Os, Ir, Pt, Re, Ru and Sn; and

each of said top electrode and said bottom electrode has a resistivity of $1m \Omega \cdot cm$ or less.

18. A method of producing an oxide dielectric element according to claim 1 or claim 2, wherein each of said top electrode and said bottom electrode is at least one kind perovskite selected from a group consisting of ReO_3 , $SrReO_3$, $BaReO_3$, $LaTiO_3$, $SrVO_3$, $CaCrO_3$, $SrCrO_3$, $SrFeO_3$, $La_{1-x}Sr_xCoO_3$ ($0 < x < 0.5$), $LaNiO_3$, $CaRuO_3$, $SrRuO_3$, $SrTiO_3$ and $BaPbO_3$; and

each of said top electrode and said bottom μ electrode has a resistivity of $1m \Omega \cdot cm$ or less.

19. A method of producing an oxide dielectric element according to any one of claims 1, 2 and 3, wherein said oxide dielectric thin film is formed by a sputtering process, a Pulsed Laser deposition process or a MOCVD (Metal Organic Chemical Vapor Deposition) process, which is performed in an atmosphere of mixed gas of oxygen and inert gas.

20. A method of producing an oxide dielectric thin film according to any one of claims 1, 2 and 3, wherein said oxide dielectric thin film is formed by a spin-coating process or a dip-coating process using metal alkoxide or organic acid salt as a starting material, said process being performed in an atmosphere of mixed gas of oxygen and inert gas at atmospheric pressure.

21. A method of producing an oxide ferroelectric element and an oxide high dielectric element according to claim 3, wherein said oxide dielectric film is re-heated by a sputtering process, a laser vapor-deposition process or a MOCVD process, which is performed in ECR oxygen plasma.

22. A method of producing an oxide dielectric element according to claim 3, wherein the re-heating treatment is performed by a spin-coating process or a dip-coating process using metal alkoxide or organic acid salt as a starting material by irradiation with light in a ultraviolet region.

23. A memory using an oxide dielectric element according to any one of claims 5, 7 and 12 as a capacitor.

24. A semiconductor device using an oxide dielectric element, characterized in that

said memory according to claim 23 is used as a non-contact type reading or writing memory.

FIG. 1

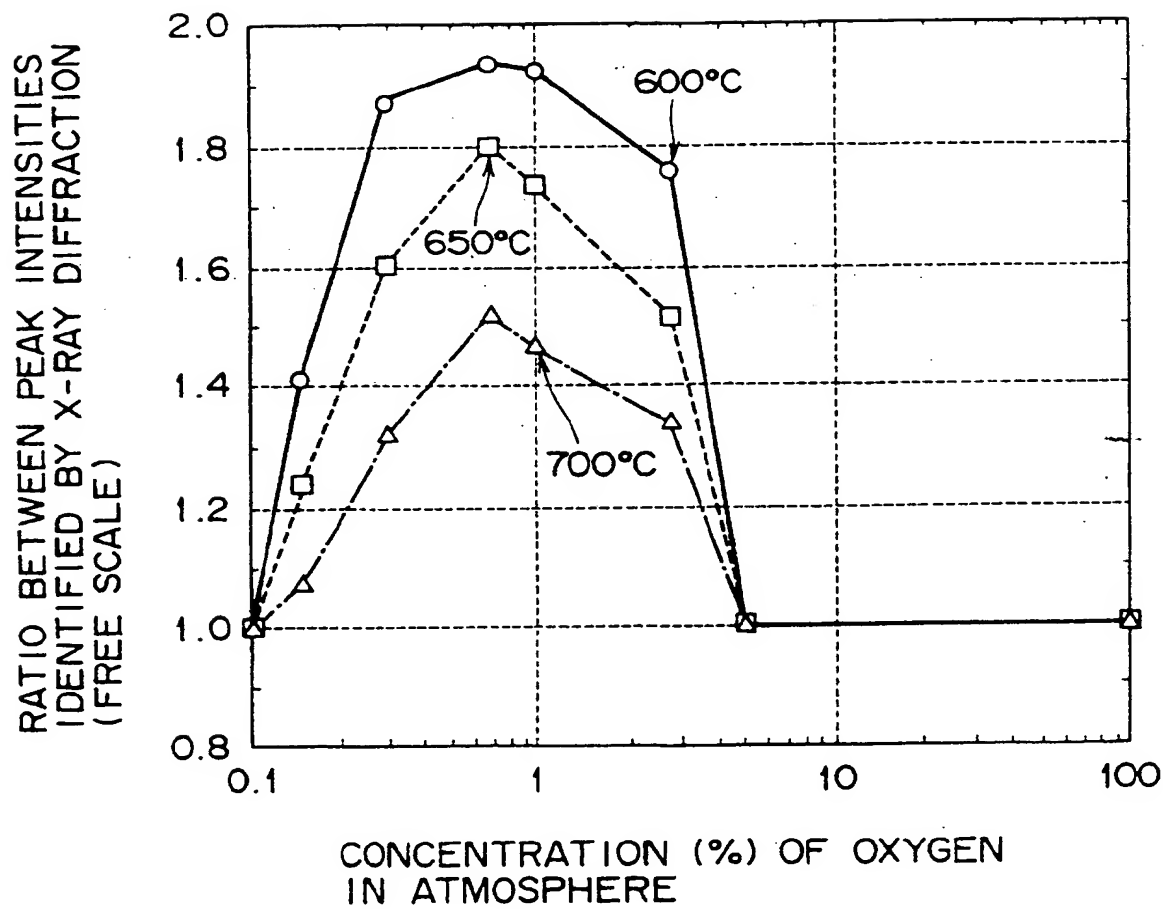


FIG. 2

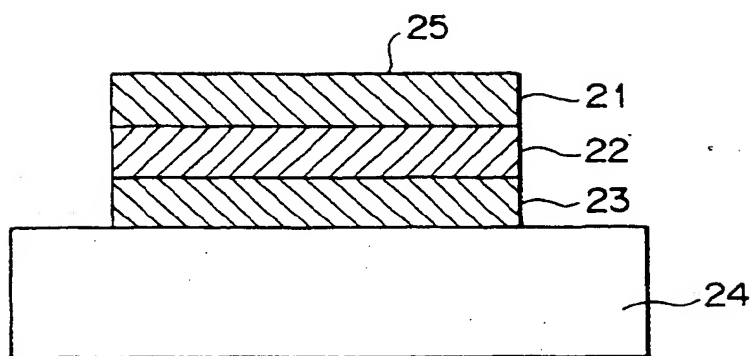


FIG. 3

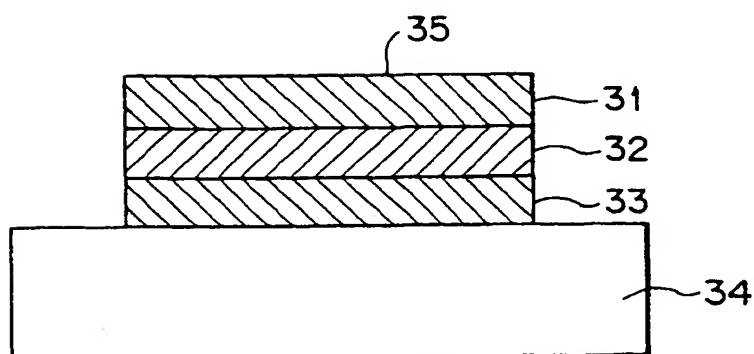


FIG. 4

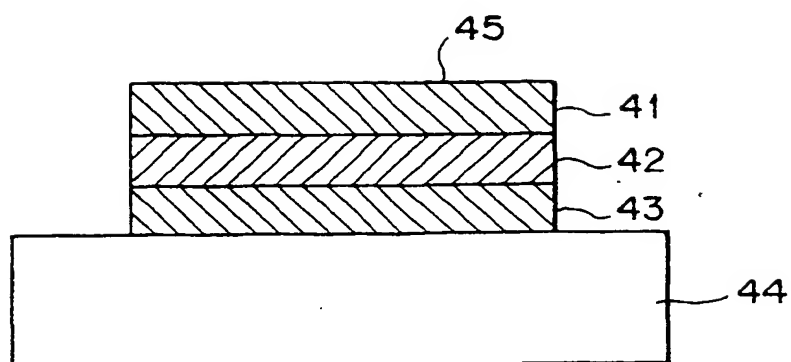


FIG. 5

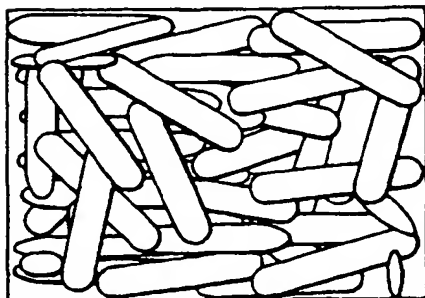


FIG. 6

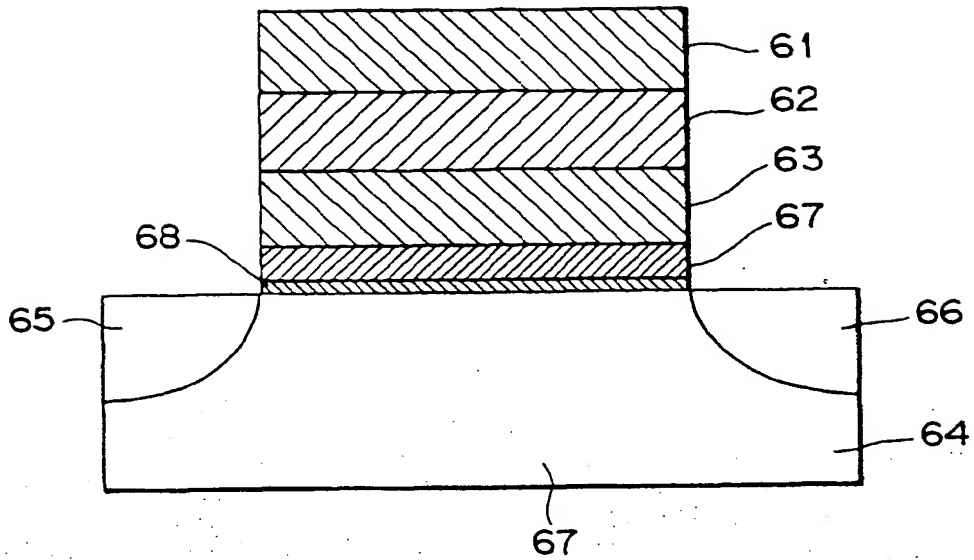
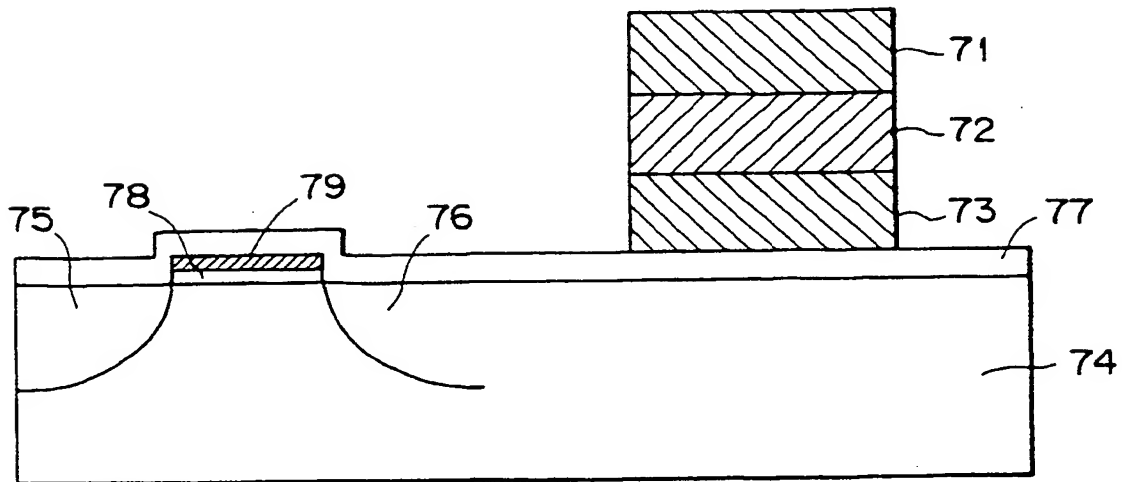


FIG. 7



SEMICONDUCTOR MOS STRUCTURE

FIG. 8

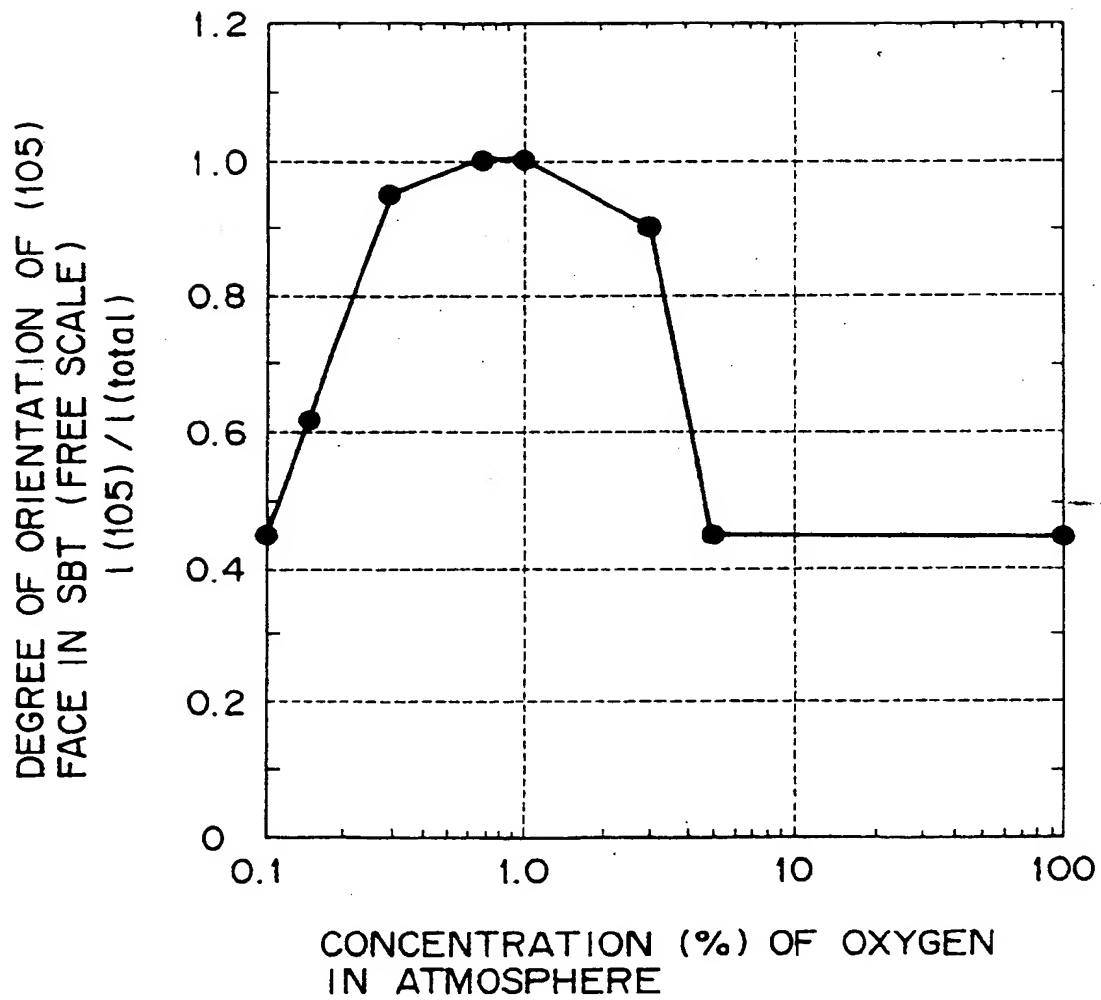


FIG. 9

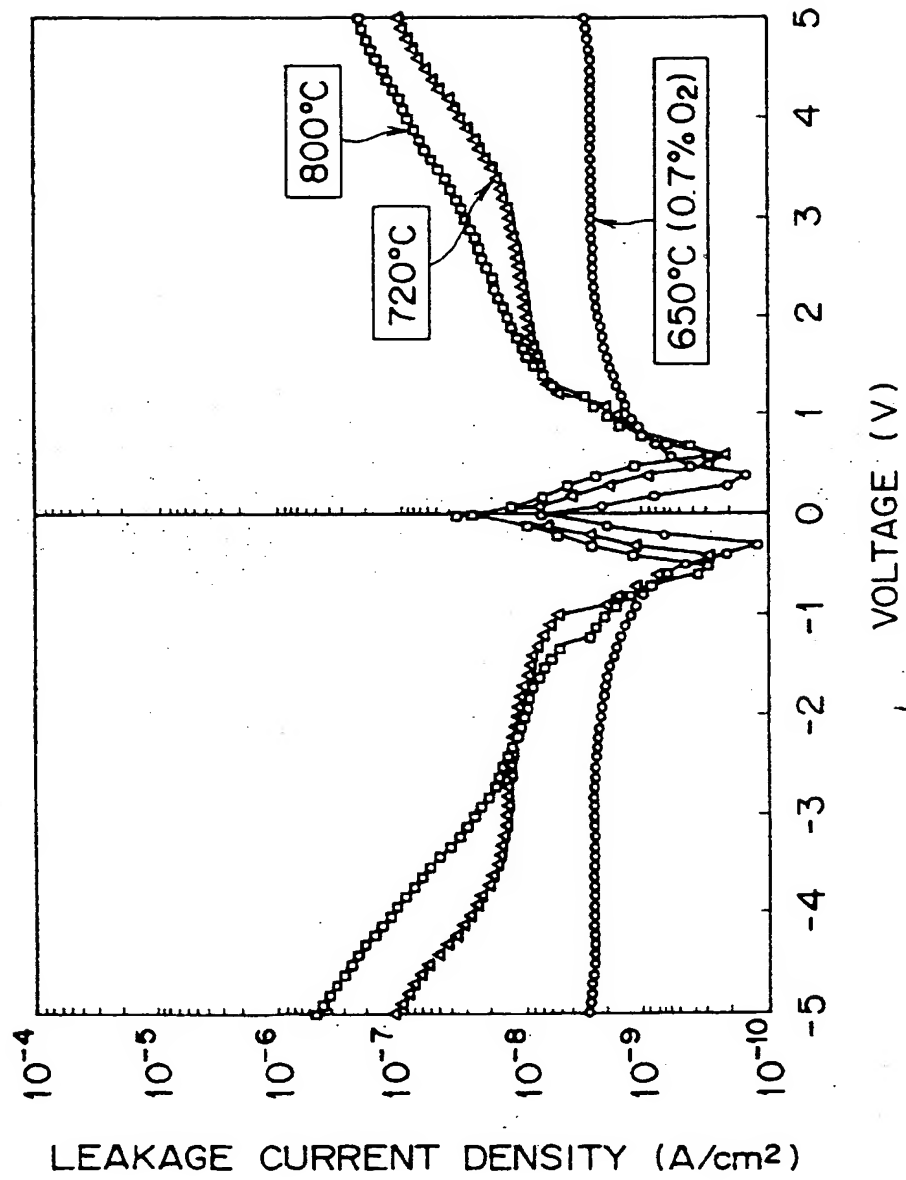


FIG. 10(a)



FIG. 10(b)

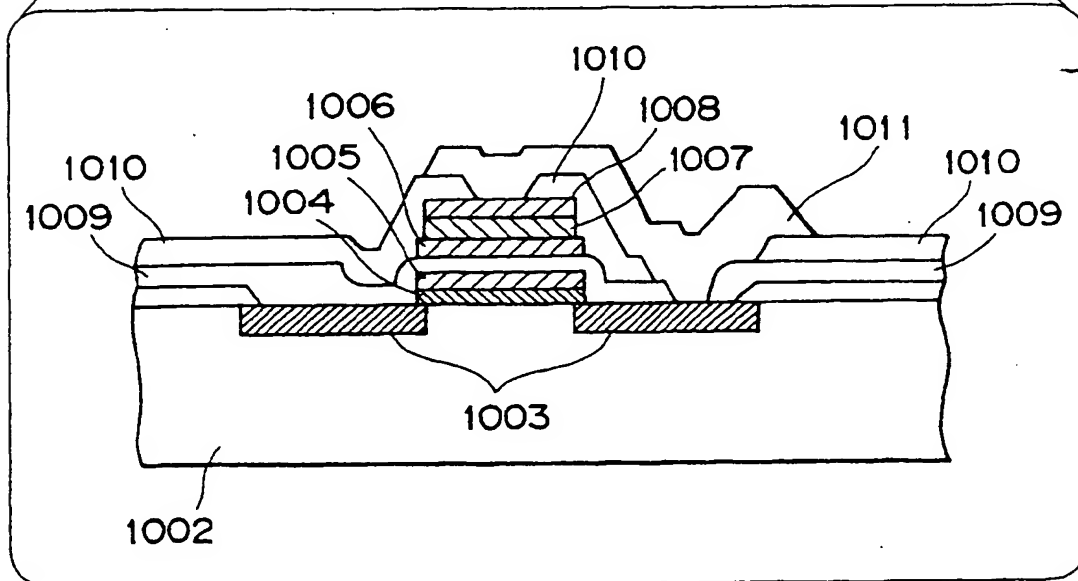
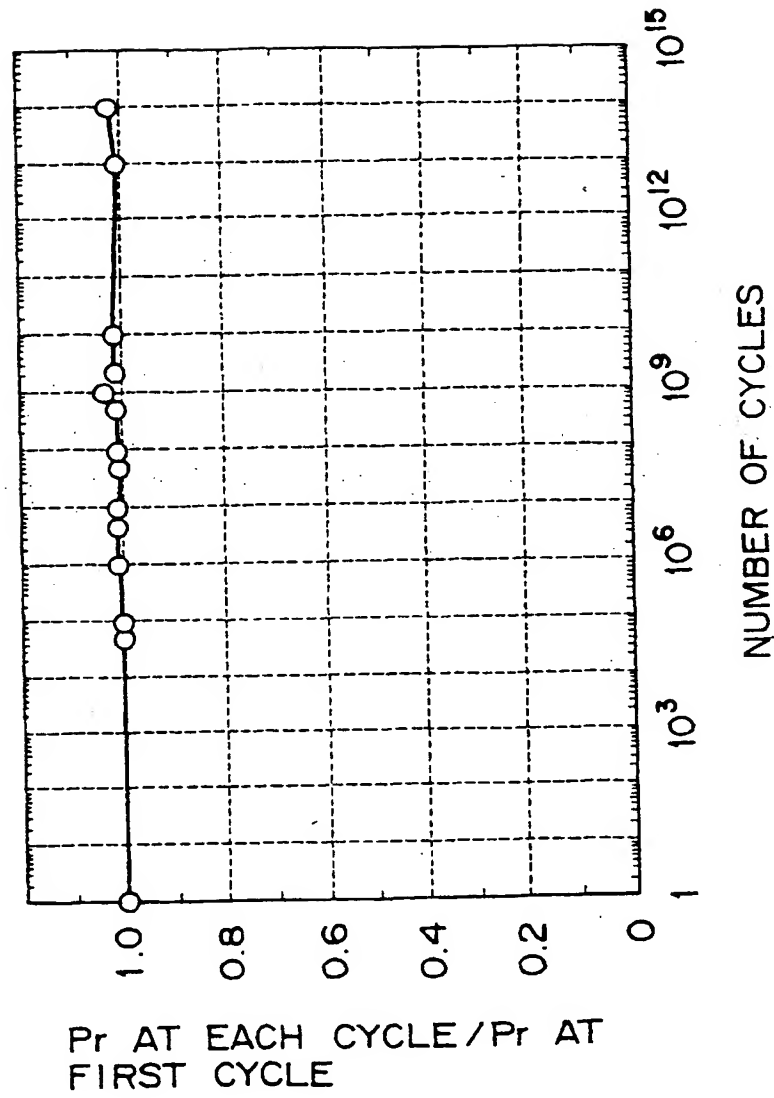


FIG. 11



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP97/00965

A. CLASSIFICATION OF SUBJECT MATTER Int. Cl ⁶ H01L21/8242, H01L21/8247, H01L27/108, H01L27/115, H01L29/788 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int. Cl ⁶ H01L21/8242, H01L21/8247, H01L27/108, H01L27/115, H01L29/788 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1922 - 1997 Kokai Jitsuyo Shinan Koho 1971 - 1997 Toroku Jitsuyo Shinan Koho 1994 - 1997 Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP, 7-172984, A (Matsushita Electric Industrial Co., Ltd.), July 11, 1995 (11. 07. 95), Page 4, left column, lines 30 to 34; Fig. 2 (Family: none)	1, 2, 6, 16
Y		3-5, 7-15, 17-24
Y	JP, 8-23073, A (NEC Corp.), January 23, 1996 (23. 01. 96), Page 3, left column, lines 12 to 14; Figs. 1, 2 (Family: none)	9-12, 20
Y	JP, 6-5946, A (Rohm Co., Ltd.), January 14, 1994 (14. 01. 94), Page 3, left column, lines 16 to 18 (Family: none)	3
Y	JP, 6-340487, A (Rohm Co., Ltd.), December 13, 1994 (13. 12. 94), Fig. 4; page 4, left column, lines 17 to 19	20, 22
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
Date of the actual completion of the international search June 17, 1997 (17. 06. 97)		Date of mailing of the international search report July 1, 1997 (01. 07. 97)
Name and mailing address of the ISA/ Japanese Patent Office Facsimile No.		Authorized officer Telephone No.

Form PCT/ISA/210 (second sheet) (July 1992)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP97/00965

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
	(Family: none)	
A	JP, 7-273216, A (Toshiba Corp.), October 20, 1995 (20. 10. 95), Fig. 1 (Family: none)	3, 21
A	JP, 8-2919, A (Sharp Corp.), January 9, 1996 (09. 01. 96), Page 5, right column, lines 3 to 9 (Family: none)	5, 7, 8, 12
A	Nikkei Microdevice, February issue, 1994, Tokyo: Nikkei Business Publications, Inc., February 1, 1994 (01. 02. 94), p. 99-103, Fig. 1(b)	5, 7, 8, 12
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